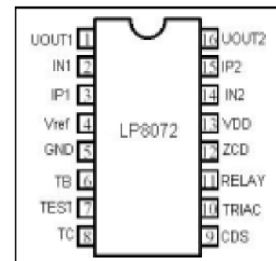


LP8072 PIR CONTROLLER

LP8072 GENERAL DESCRIPTION

LP8072 is a PIR (passive infra-red) controller, using analog mixing digital design technique and manufactures by CMOS process which can either drive TRIAC or RELAY depending on users' choice. With special noise immunity technique, LP8072 is the most stable PIR controller you can find on the market. More than this, there are few components needed in its application circuit which can reduce material cost and increase competitive.



LP8072 PIN DESCRIPTION

Pin No.	Pin Name	I/O	Description
1	UOU1		First stage OP amp output
2	NI1		First stage OP amp positive input
3	II1		First stage OP amp negative input
4	VREF		Stable reference voltage
5	GND		System ground
6	TB		Time base for 1. The delay time of receiving PIR signal to sent a puls to trigger TRIAC or a high signal to trigger relay. The delay time = $R \times C \times 32$. The PIR signal patented and accepted only if the signal cycle greater than $R \times C \times 768$. When state of relay or TRIAC is changing form active into inactive mode. It takes more than $R \times C \times 4069$, then system is able to receive PIR signal again. 2.The flash cycle show the beginning of auto mode. Note: Width of TRIAC pulse = $R \times C \times 2$. Flash cycle: $R \times C \times 32768$ $10\text{ K} < R < 1\text{M Ohm}$ $100\text{pF} < C < 0.1\text{ uF}$ (Reference Diagram 1)
7	QTEST		For testing only
8	TCI		To set up the timing of how long Triac or relay is active. During the period, if the system receives the PIR signal, then it restarts counting the timing again. The range for R: $10\text{K} < R < 1\text{M Ohm}$ C: $100\text{uF} < C < 0.1\text{ uF}$ (Reference Diagram 2)
9	CDS		Connected to a CDS for inhibiting relay or TRIAC being triggered. If TRIAC or relay has already being triggered by PIR signal and turned into active mode, then CDS can not inhibit PIR again.
10	TRIAC		To trigger TRIAC, active low Sink current: 15 mA max.
11	RELAY		To drive relay, active high Sink current: 10 mA max. Source current: 10 mA max.
12	ZCD		Detect zero cross of AC line under remote mode function.
13	VDD		Operation voltage: 5V, stand by current: 1mA
14	II2		2nd stage OP amp negative input

Silvan Chip Electronics Tech.Co.,Ltd.

Address: Fl.16 FuChang Building,Binhe Rd,Shenzhen,China

Phone: 86-0755-88291843 88304213 Fax : 86-0755-88290509

Email: jj@sc-tech.cn Http:// www.sc-tech.cn

LP8072 PIR CONTROLLER

15	NII2	2 nd stage OP amp positive input
16	UOU2	2 nd stage OP amp output

LP8072 Absolute Maximum Ratings

Parameter	SYMBOL	VALUE	Units
POWER SUPPLY V _{dd} WITH REPECT TO V _{ss}	V _{dd} -V _{ss}	5.6	V
VOLTAGE ON ANY PIN		-0.3-5.6	V
OPERATING TEMPERATURE	T _{op}	0-70	C
STORAGE TEMPERATURE		-65-150	C

LP8072 ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
V _{dd}	SUPPLY VOLTAGE		4.2	5	5.5	V
I _{st}	STANDY CURRENT		0.9	1.0	1.2	mA
I _{dd}	OPERATING CURRENT	1.8mA, TRIAC 2.5mA, RELAY	1.8		2.5	mA
V _{ref}	STABLE VOLTAGE	V _{dd} >4.2V	3.0	3.2	3.4	V
I _{ref}	SOURCE CURRENT OF V _{ref}		200			uA
	RIPPLE OF V _{ref}				0.5	mV
	INPUT AND OUTPUT REGULATION OF V _{ref}				0.3%	
F _{tb}	TIME BASE OPERATING FREQUENCY		15	16	17	KHZ
V _{t+}	CDS OPERATING TRIGGER		1.3	1.7	2.1	V
V _{t-}	CDS OPREATING TRIGGER		0.6	0.9	1.1	V
I _{cds}	CDS SOURCE CURRENT		2.6	3.5	4.4	uA
I _{source}	CDS OUTPUT SOURCE CURRENT		9	10.4	17.4	mA
I _{sink}	CDS OUTPUT SINK CURRENT		11.6	13	21	mA
I _{rs}	RELAY SOURCE CURRENT				5	mA
I _{rsink}	RELAY SINK CURRENT				5	mA

Silvan Chip Electronics Tech.Co.,Ltd.

Address: Fl.16 FuChang Building,Binhe Rd,Shenzhen,China

Phone: 86-0755-88291843 88304213 Fax : 86-0755-88290509

Email: jj@sc-tech.cn Http:// www.sc-tech.cn

